

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	232	((network crypto\$ cipher\$ encrypt\$ secur\$) near processor) and (buffer with (switch\$ near fabric))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:49
L2	2	!1 and 713/151-154,168-170,189,190.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:50
L3	1	!1 and 380/28-30,37,42.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:50
L4	1	!1 and 726/11-14.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:50
L5	19	!1 and 709/230-235,239.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:50
L6	18	!1 and 370/218,351-357,360.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:50

L7	1	L1 and 380/255-257.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:50
L8	184	((switch\$3 adj fabric) with (plurality plural multi multi\$3) near2 (buffers storage register cach))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51
L9	48	L8 and 370/386,389,391,395.1,395.7,395.72,400,413,415,417,419.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51
L10	2430	380/28-30,37,42.ccls. and ((network crypto\$5 cipher\$3 encrypt\$3 secur\$3) near (processor controller module device system apparatus circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51
L11	3	L10 and (align\$4 near (buffer storage memory cach register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51
L12	1096815	((stor\$3 fil\$3 transmit\$4 send\$3 forward\$3) near2 (data packet cell chunk segment fragment part portion)) with (memory buffer cach queue storage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51
L13	3327	713/151-154,168-170,189,190.ccls. and L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51

L14	2505	L13 and ((network crypto\$5 cipher\$3 encrypt\$3 secur\$3) near (processor controller module device system apparatus circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51
L15	12	L14 and (align\$4 near (buffer storage memory cach register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/21 09:51

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